

	Type	L #	Hits	Search Text	DBs
1	BRS	L2	15619	(flip adj flop latch) same (mux multiplexer)	US-PGPUB; USPAT
2	BRS	L3	7575	2 same clock	US-PGPUB; USPAT
3	BRS	L4	506	3.clm.	US-PGPUB; USPAT
4	BRS	L5	1110	3 same inverter	US-PGPUB; USPAT
5	BRS	L6	1015	5 not 4	US-PGPUB; USPAT
6	BRS	L7	308	6 and (flip adj flop latch).clm.	US-PGPUB; USPAT
7	BRS	L8	11104	(flip adj flop latch) with (mux multiplexer)	US-PGPUB; USPAT
8	BRS	L9	645	8 with inverter	US-PGPUB; USPAT
9	BRS	L10	221	9 not (3 7)	US-PGPUB; USPAT
10	BRS	L11	483	9 not (4 7)	US-PGPUB; USPAT
11	BRS	L12	437	11 and clock	US-PGPUB; USPAT
12	BRS	L13	3107	(flip adj flop)near5 (mux multiplexer)	US-PGPUB; USPAT
13	BRS	L14	168	13 with inverter	US-PGPUB; USPAT
14	BRS	L15	2399	(flip adj flop latch) same (mux multiplexer)	USOCR; EPO; JPO; DERWENT
15	BRS	L17	852	15 and clock	USOCR; EPO; JPO; DERWENT
16	IS&R	L18	2427	(327/291-299).CCLS.	US-PGPUB; USPAT
17	BRS	L19	262	18 and (flip adj flop latch) and (mux multiplexer)	US-PGPUB; USPAT
18	BRS	L20	77531	(flip adj flop latch).clm.	US-PGPUB; USPAT
19	BRS	L21	3184	20 and (mux multiplexer).clm.	US-PGPUB; USPAT
20	BRS	L22	1880	21 and clock.clm.	US-PGPUB; USPAT
21	BRS	L23	1682	22 not (7 14 19)	US-PGPUB; USPAT